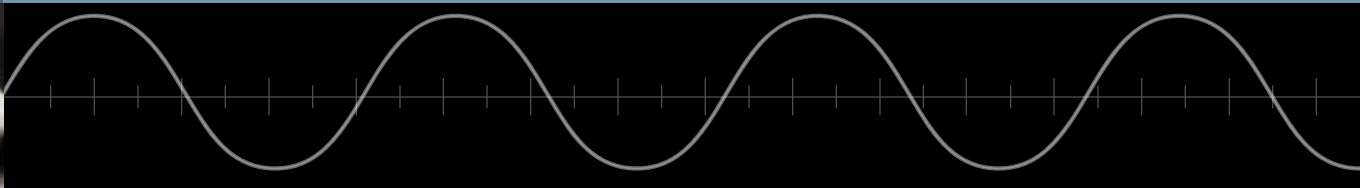


# Power Solutions used on Altera Stratix V FPGA Boards



## • Stratix V GX Signal Integrity Kit

- **Core Power:** LTC3855
- **High Speed I/O Power:** LTM4608, LTC3026
- **Digital Power Management:** LTC2978

- **Implementation on Altera Board:**

[http://www.altera.com/literature/manual/rm\\_svgx\\_si\\_dev\\_board.pdf#page=17](http://www.altera.com/literature/manual/rm_svgx_si_dev_board.pdf#page=17) (page 17)

- **Development Kit Landing Page on Altera Website:**

<http://www.altera.com/products/devkits/altera/kit-transceiver-si-stratix-v.html>

- **Altera PDF File for Power Tree:**

[http://www.altera.com/literature/manual/rm\\_svgx\\_si\\_dev\\_board.pdf#page=50](http://www.altera.com/literature/manual/rm_svgx_si_dev_board.pdf#page=50) (page 50)

- **Schematic & Layout Files:**

[ftp://ftp.altera.com/outgoing/devkit/11.1/stratixVGX\\_5sgxea7nf40es\\_si\\_v11.1.2.exe](ftp://ftp.altera.com/outgoing/devkit/11.1/stratixVGX_5sgxea7nf40es_si_v11.1.2.exe)  
(install file to extract board files)



## • Stratix V GT Signal Integrity Kit

- **Core Power:** LTC3855
- **HSIO Power:** LTM4608, LTC3026
- **Digital Power Management:** LTC2978

- **Implementation on Altera Board:**

[http://www.altera.com/literature/ug/ug\\_svgt\\_si\\_dev\\_kit.pdf#page=44](http://www.altera.com/literature/ug/ug_svgt_si_dev_kit.pdf#page=44) (page 44)

- **Development Kit Landing Page on Altera Website:**

- <http://www.altera.com/products/devkits/altera/kit-sv-gt-si.html>

- **Altera PDF File for Power Tree:**

[http://www.altera.com/literature/ug/ug\\_svgt\\_si\\_dev\\_kit.pdf#page=48](http://www.altera.com/literature/ug/ug_svgt_si_dev_kit.pdf#page=48) (page 48)

- **Schematic & Layout Files:**

[ftp://ftp.altera.com/outgoing/devkit/11.1/stratixVGT\\_5sgtmc7kf40es\\_si\\_v11.1.2.exe](ftp://ftp.altera.com/outgoing/devkit/11.1/stratixVGT_5sgtmc7kf40es_si_v11.1.2.exe) (install file to extract, please note that this is for ES silicon)



- **Stratix V PCIe Development Kit**

- **Core Power:** LTC3880
- **HSIO Power:** LTM4614, LT3080
- **Development Kit Landing Page on Altera Website:**  
<http://www.altera.com/products/devkits/altera/kit-sv-gx-host.html>
- **Altera PDF File for Power Tree:**  
[http://www.altera.com/literature/manual/rm\\_svgx\\_fpga\\_dev\\_board.pdf#page=66](http://www.altera.com/literature/manual/rm_svgx_fpga_dev_board.pdf#page=66) (page 66)
- **Schematic & Layout Files:**  
[ftp://ftp.altera.com/outgoing/devkit/11.1/stratixvgx\\_5sgxea7kf40es\\_fpga\\_v11.1.2.exe](ftp://ftp.altera.com/outgoing/devkit/11.1/stratixvgx_5sgxea7kf40es_fpga_v11.1.2.exe) (install file to extract board files)

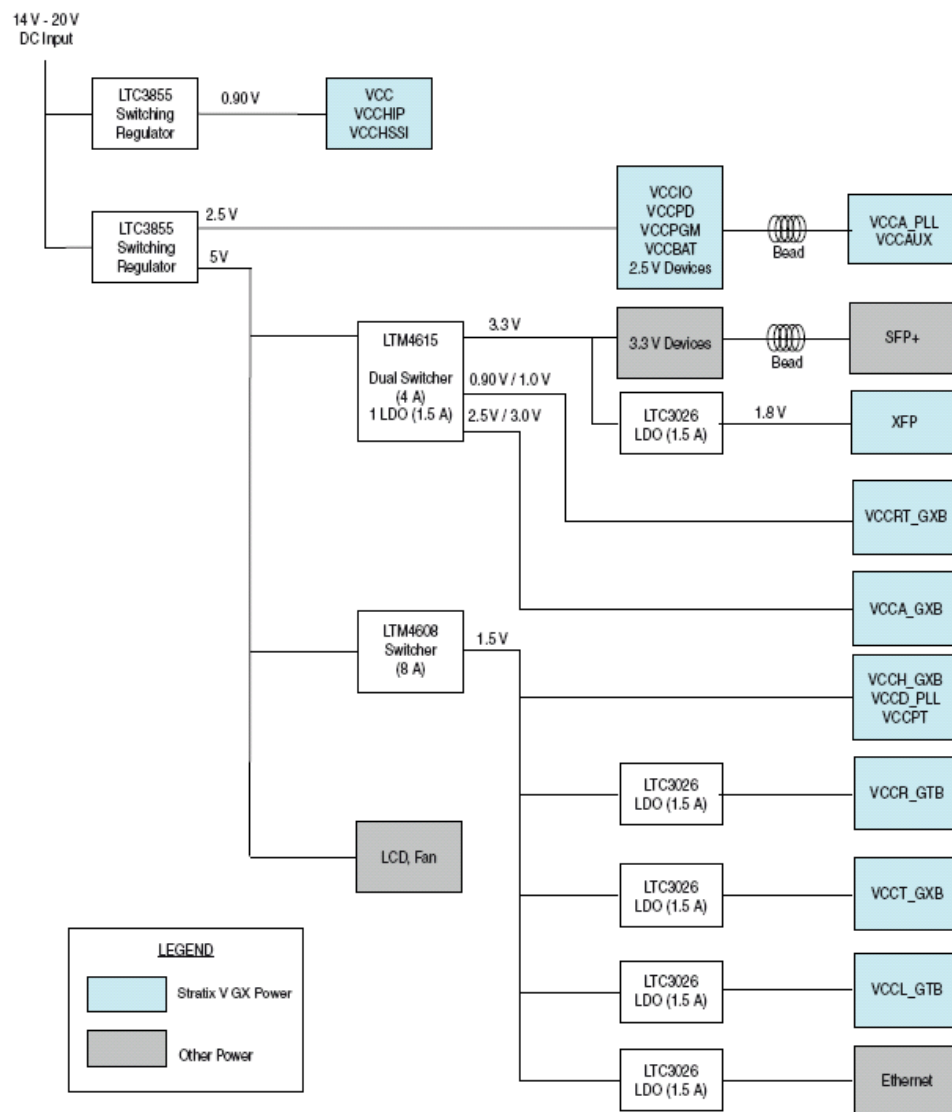


- **Stratix V GX 100G Development Kit**

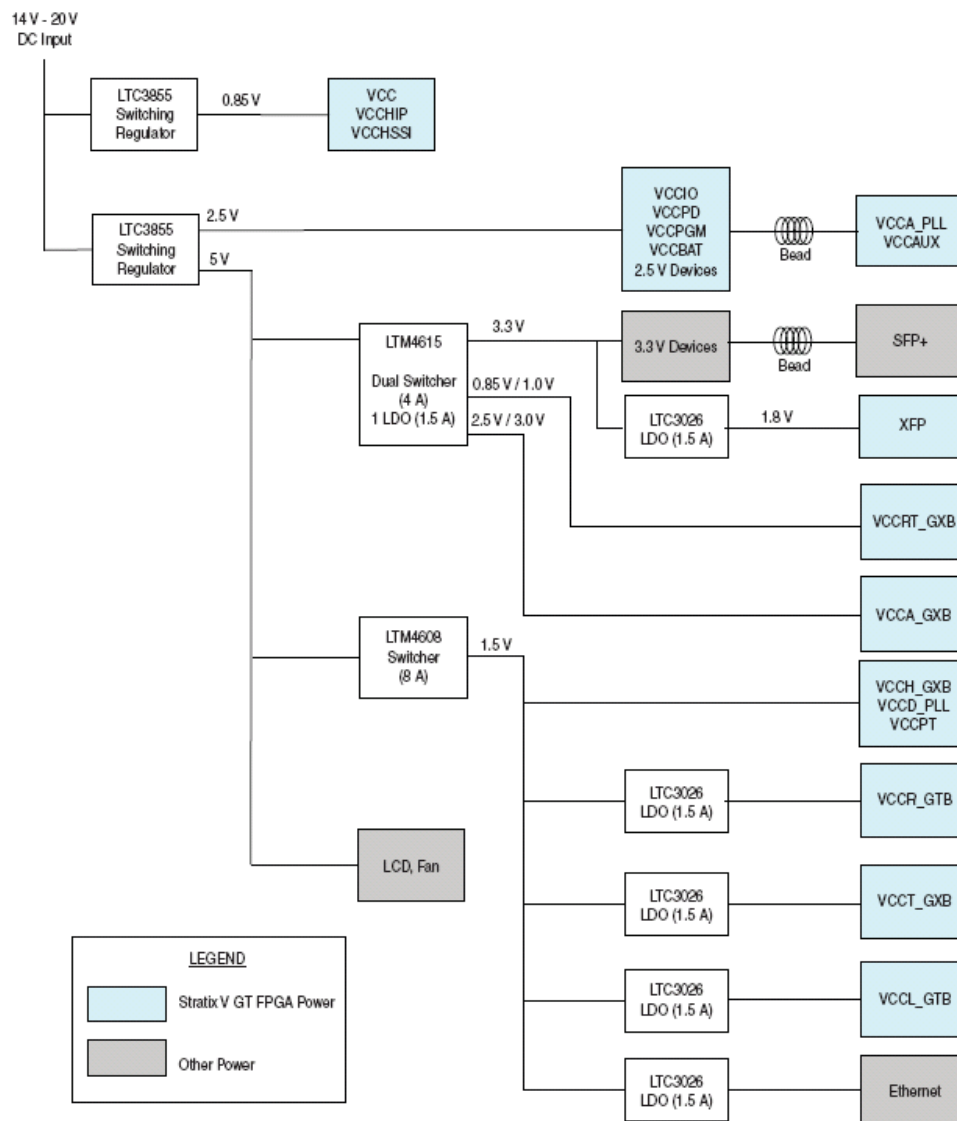
- **Core Power Parts:** LTM4627 x 4
- **HSIO Parts:** LT3070, LTC3026
- **Development Kit Landing Page on Altera Website:**  
<http://www.altera.com/products/devkits/altera/kit-stratix-v-gx-100g.html>
- **Altera PDF File for Power Tree:**  
[http://www.altera.com/literature/manual/rm\\_svgx\\_100g\\_dev\\_board.pdf#page=71](http://www.altera.com/literature/manual/rm_svgx_100g_dev_board.pdf#page=71) (page 71)
- **Schematic & Layout Files:**  
[ftp://ftp.altera.com/outgoing/devkit/11.1/stratixVGX\\_5sgxea7nf45\\_100g\\_v11.1.2.exe](ftp://ftp.altera.com/outgoing/devkit/11.1/stratixVGX_5sgxea7nf45_100g_v11.1.2.exe) (install file to extract board files)



# Stratix V GX Signal Integrity Kit Power Tree



# Stratix V GT Signal Integrity Kit Power Tree



- 1 VCC/VCCHIP/VCCSSI
- 2 VCCPD/VCCPGM/VCCAUX/VCCA\_FPLL
- 3 VCCIO
- 4 VCCR\_GXB/VCCCT\_GXB
- 5 VCCA\_GXB
- 6 VCCPT/VCCCH\_GXB/VCCD\_FPLL



# Stratix V GX 100G Development Kit Power Tree

