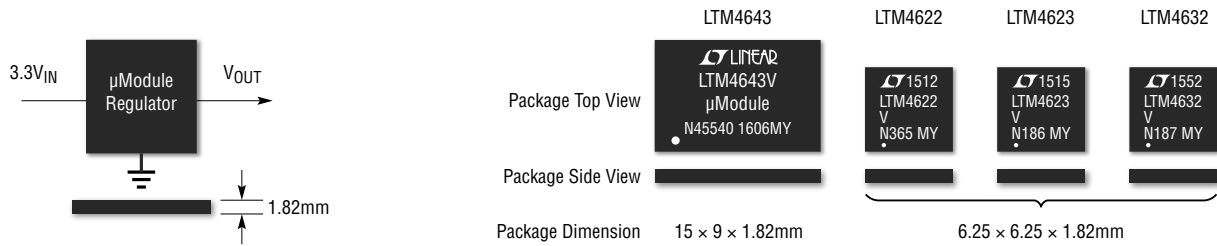


$\leq 3.3V_{IN}$ μ Module Regulators

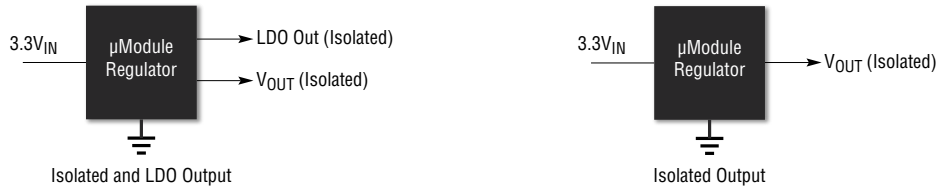
Ultrathin Step-Down μ Module[®] Regulators



# of Outputs	LGA Package Height (mm)	Input Voltage (V)		Output Voltage (V)		Output Current per Channel (A)	Parallel Operation	Clock Sync Range (MHz)	Package Dimensions (mm)	Package	Part Number
		Min	Max	Min	Max						
4	1.82	2.375*	20	0.6	3.3	Quad : 3	Yes	0.7 to 1.3	9 x 15 x 1.82	LGA	LTM®4643
2		3.1	20	0.6	5.5	Dual: 2.5, 2.5	Yes	0.56 to 4	6.25 x 6.25 x 1.82 6.25 x 6.25 x 2.42	LGA BGA	LTM4622
1		2.375*	20	0.5	5.5	3	Yes	0.56 to 4	6.25 x 6.25 x 1.82 6.25 x 6.25 x 2.42	LGA BGA	LTM4623
For DDR, QDR Termination (VDDQ, VTT and VREF)											
3	1.82	3.1	15	0.6	2.5	Triple 3, 3, 10mA	Yes	0.56 to 4	6.25 x 6.25 x 1.82 6.25 x 6.25 x 2.42	LGA BGA	LTM4632

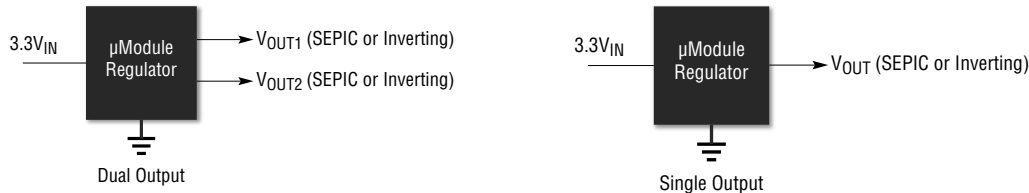
* Requires external bias voltage

Isolated μ Module Regulators



# of Outputs	Function	Isolation Voltage	Input Voltage (V)		Output Voltage (V)		Output Power (W)	Package Dimensions (mm)	Package	Part Number
			Min	Max	Min	Max				
1	Isolated	2kVAC	2.8	40	2.5	24	2.25	9 x 11.25 x 4.92	BGA	LTM8067
2	Isolated with LDO		2.8	40	1.2	18				LTM8068

SEPIC or Inverting μ Module Regulators (SEPIC: Single-Ended Primary Inductance Converter)



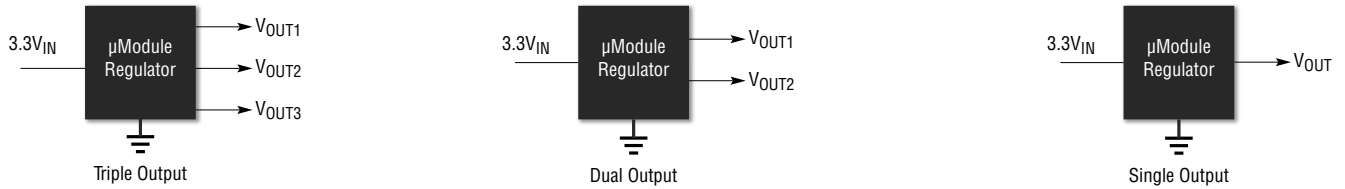
# of Outputs	Input Voltage (V)		Output Voltage (V)		I _{OUT} at 12V _{IN} to 5V _{OUT} (A)	Package Dimensions (mm)	Package	Part Number
	Min	Max	Min	Max				
2	2.6	20	±2.5	±24	1	9 x 15 x 2.42	BGA	LTM8049
1	2.8	18	±2.5	±15	0.6	6.25 x 11.25 x 4.92	BGA	LTM8045



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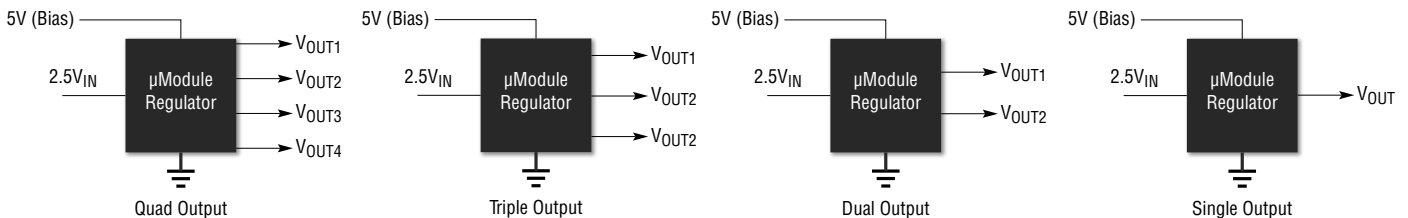
$\leq 3.3V_{IN}$ μ Module Regulators

Step-Down μ Module Regulators (External Bias Voltage Not Required)



# of Outputs	Input Voltage (V)		Output Voltage (V)		Output Current per Channel (A)	Parallel Operation	Clock Sync Range (MHz)	Package Dimensions (mm)	Package	Part Number
	Min	Max	Min	Max						
3	2.375	5.5	0.8	5	Triple: 4, 4, 1.5	Yes	–	15 x 15 x 2.82	LGA	LTM4615
2	3.1	20	0.6	5.5	Dual: 2.5, 2.5	Yes	0.56 to 4	6.25 x 6.25 x 1.82 6.25 x 6.25 x 2.42	LGA BGA	LTM4622
	2.375	5.5	0.8	5	Dual: 4, 4	Yes	–	15 x 15 x 2.82	LGA	LTM4614
	2.7	5.5	0.6	5	Dual: 8, 8	Yes	0.75 to 2.25	15 x 15 x 2.82 15 x 15 x 3.42	LGA BGA	LTM4616
	3	36	0.8	5	0.5	–	–	6.25 x 11.25 x 2.82	LGA	LTM8021
1	2.375	5.5	0.8	5	4	Yes	–	9 x 15 x 2.32 9 x 15 x 3.42	LGA BGA	LTM4604A
	2.7	5	0.6	5	8	Yes	0.75 to 2.25	9 x 15 x 2.82	LGA	LTM4608A
	2.375	5	0.6	5	10	Yes	0.35 to 0.65	9 x 15 x 4.92	BGA	LTM4648
	1.5	5.5	0.8	5	15	Yes	0.36 to 0.71	15 x 15 x 4.32	LGA	LTM4611
For DDR, QDR Termination										
3	3.1	15	0.6	2.5	Triple: 3, 3, 10mA	Yes	0.56 to 4	6.25 x 6.25 x 1.82 6.25 x 6.25 x 2.42	LGA BGA	LTM4632

Step-Down μ Module Regulators (External Bias Voltage Required)



# of Outputs	Input Voltage (V)		Output Voltage (V)		Output Current per Channel (A)	Parallel Operation	Clock Sync Range (MHz)	Package Dimensions (mm)	Package	Part Number
	Min	Max	Min	Max						
4	2.375	20	0.6	3.3	Quad: 3	Yes	0.7 to 1.3	9 x 15 x 1.82	LGA	LTM4643*
	2.375	14	0.6	5.5	Quad: 4	Yes	0.7 to 1.3	9 x 15 x 5.01	BGA	LTM4644*
3	2.375	16	0.8	1.8, 5.5	Triple: 10	Yes	0.6 to 0.75	15 x 15 x 5.01	BGA	LTM4633
2	2.375	20	0.5	5.5	Dual: 4	Yes	0.6 to 1.4	9 x 11.25 x 4.92	BGA	LTM4642
1	2.375	20	0.5	5.5	3	Yes	0.56 to 4	6.25 x 6.25 x 1.82 6.25 x 6.25 x 2.42	LGA BGA	LTM4623
	2.375	14	0.6	5.5	4	–	–	6.25 x 6.25 x 5.01	BGA	LTM4624
	2.375	20	0.6	5.5	5	Yes	0.56 to 4	6.25 x 6.25 x 5.01	BGA	LTM4625
	2.375	7	0.6	5.5	20	Yes	0.25 to 0.8	15 x 15 x 4.92	BGA	LTM4639

*Pin Compatible