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Lowest Noise, 16-Bit 20Msps ADCs Achieve 84dB SNR & 46 Microvolt (RMS) Input-Referred Noise

MILPITAS, CA – October 22 , 2012 – Linear Technology Corporation introduces three low power 16-bit, 20Msps analog-to-digital converters (ADCs), the [LTC2269](#), [LTC2270](#) and [LTC2271](#), offering the lowest input-referred noise and tight integral nonlinearity error (INL) for very high precision DC measurements. With only $46\mu\text{V}_{\text{RMS}}$ input noise and maximum guaranteed INL error of $\pm 2.3\text{LSB}$, these ADCs are suitable for very low noise, high linearity sampling applications such as digital x-ray, infrared and medical imaging, pachymeters, spectrometry and cytometry. These devices achieve signal-to-noise ratio (SNR) performance of 84dB and SFDR of 99dB at baseband. High AC performance and low noise is achieved using a well designed 2.1Vp-p front end, which also significantly lowers the power required by the ADC driver circuitry. The ADCs themselves consume approximately 80mW/channel. Further power savings can be achieved by placing the devices in standby (12mW) or shutdown (0.5mW), making them ideal for handheld test and measurement applications.

The LTC2269 and LTC2270 are single-channel and two-channel simultaneous sampling parallel ADCs, respectively, offering a choice of full-rate CMOS, or double data rate (DDR) CMOS/LVDS digital outputs with programmable digital output timing, programmable LVDS output current and optional LVDS output termination. The LTC2271 includes two-channel, simultaneous sampling ADCs with serial LVDS outputs. They include Linear Technology's digital output randomizer and alternate bit polarity (ABP) mode that minimize digital feedback in the application.

These low power 16-bit ADCs provide a pin-compatible upgrade to the existing LTC2160, LTC2180, LTC2190 families of 25Msps to 125Msps 1.8V low power ADCs. Available in compact QFN packages, designers can benefit from the flexible choice of interfaces to minimize pin count and ease routing to FPGAs. These ADCs are now in production, with evaluation boards and samples available online or from your local Linear Technology sales office. Pricing starts at \$30.00 each for the single LTC2269 device in 1,000-piece quantities. The complete product family can be found at: www.linear.com/HSADC


Photo Caption: Lowest Noise 16-bit 20Msps ADCs

Summary of Features: LTC2270

- 16-bit, 20Msps Single & Dual ADCs
- 84dB SNR, 99dB SFDR
- $46\mu\text{V}_{\text{RMS}}$ Input-Referred Noise
- $\pm 2.3\text{LSB}$ (max) INL Error
- Low Power: 80mW/Ch
- Single 1.8V Supply
- Flexible Digital Interfaces:
 - CMOS, DDR CMOS or DDR LVDS Outputs
 - Serial LVDS
- Selectable Input Ranges: $1\text{V}_{\text{P-P}}$ to $2.1\text{V}_{\text{P-P}}$
- 200MHz Full-Power Bandwidth S/H
- Optional Data Output Randomizer
- Optional Clock Duty Cycle Stabilizer
- Shutdown & Nap Modes
- Serial SPI Port for Configuration

About Linear Technology

Linear Technology Corporation, a member of the S&P 500, has been designing, manufacturing and marketing a broad line of high performance analog integrated circuits for major companies worldwide for three decades. The Company's products provide an essential bridge between our analog world and the digital electronics in communications, networking, industrial, automotive, computer, medical, instrumentation, consumer, and military and aerospace systems. Linear Technology produces power management, data conversion, signal conditioning, RF and interface ICs, μ Module[®] subsystems, and wireless sensor network products. For more information, visit www.linear.com

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