

USB Compatible Li-Ion Battery Charger and Dual Buck Regulators in a Single 3mm × 3mm QFN

by Aspiyan Gazder

Introduction

Manufacturers of handheld devices such as MP3 players are always looking to reduce system size and cost, even as they increase performance and functionality. The only way to do so is to integrate functions at the IC level. For applications powered from a single Li-Ion cell, the LTC3559 provides a single chip solution that charges a Li-Ion cell while efficiently generating two supply voltage rails to power the device.

The LTC3559 is a USB compatible battery charger and two monolithic synchronous buck regulators integrated into a low profile 3mm × 3mm 16-lead QFN package. The battery charger has all the features that a stand alone battery charger might offer, such as an NTC input for temperature qualified charging, internal timer termination and bad battery

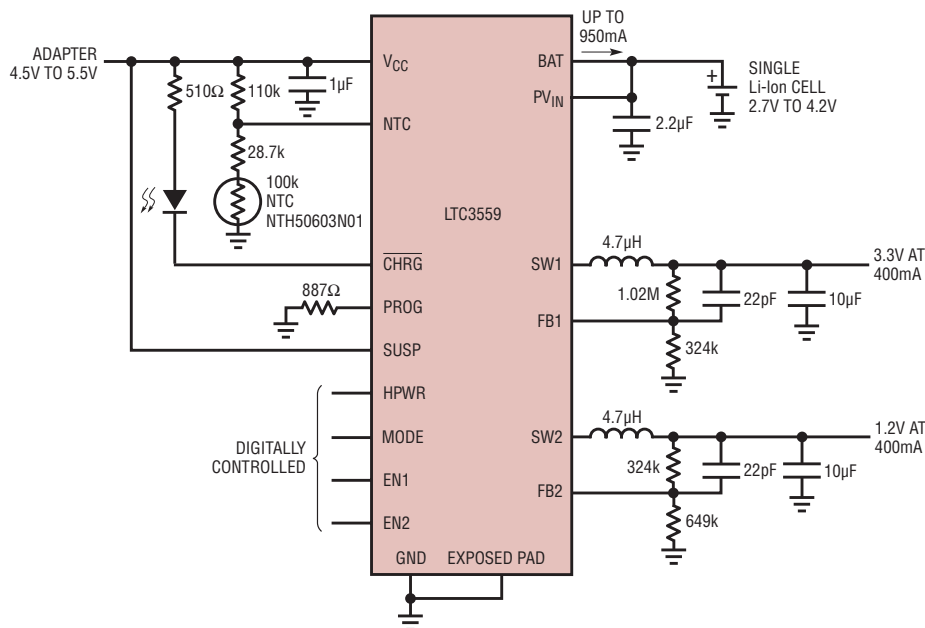


Figure 1. Full featured USB battery charger and dual buck regulator in one 3mm × 3mm IC

detection. A constant current/constant voltage algorithm is employed to charge a battery. Only a single resistor at the PROG pin is required to program the charge current up to 950mA. The HPWR input provides the flexibility to deliver either 100% or 20% of the programmed charge current. For applications operating from a USB source, charge current can be programmed to either 100mA or 500mA per USB specifications.

The two buck regulators have a current mode architecture, which provides a quick response to load steps. To meet the noise and power requirements of a variety of applications, the buck regulators can be operated in either Burst Mode operation or pulse skipping mode. The buck regulators also have a soft start feature that prevents large inrush currents at start up.

At high load currents, the buck regulator operates as a constant frequency PWM controlled regulator. At

light load currents, pulse skipping is the normal behavior for a switching regulator when the inductor current is not allowed to reverse.

To improve efficiency in light load conditions, the LTC3559 offers Burst Mode operation. When in Burst Mode operation, the buck regulator automatically switches between fixed frequency PWM control or hysteretic control, as a function of the load current. At light loads, the regulator has an output capacitor charging phase followed by a sleep phase. During the sleep phase, most of the buck regulators' circuitry is powered down, saving battery power. As the load current increases, the sleep time decreases to the point where the buck regulator switches to a constant frequency PWM operating mode—equivalent to pulse skipping mode at higher output currents.

Figure 1 shows the LTC3559 with the NTC input biased using three resistors. A 3-resistor bias provides the user

DESIGN IDEAS

USB Compatible Li-Ion Battery Charger and Dual Buck Regulators in a Single 3mm × 3mm QFN.....27
Aspiyan Gazder

Entire RGB LED Driver Fits in Miniscule 3mm × 2mm Package29
Zachary Lewko

USB Power Manager with High Voltage 2A Bat-Track™ Buck Regulator.....30
Nancy Sun

Complete 3-Rail Power Supply in a 4mm × 4mm QFN Package.....32
John Canfield

PC Quad Buck Regulator Packs Performance, Functionality, Versatility and Adaptability in a 3mm × 3mm QFN34
Joe Pangniban

μModule Regulators Shrink Power Supply Size and Design Effort.....36
David Ng

Small, High Efficiency Solution Drives Two Piezo Motors.....38
Wei Gu

with the flexibility to program both the upper and lower battery temperature points that are considered safe for charging the battery. In this example, the NTC hot and cold trip points are set for approximately 55°C and 0°C, respectively.

One of the buck regulators is programmed for 3.3V at its output. When the BAT pin voltage approaches 3.3V, the buck regulator operates in dropout. An LED at the CHRG pin gives a visual indication of the battery charge status.

Figure 2 shows an actual circuit similar to that shown in Figure 1, illustrating how little board space is required to build a full featured LTC3559 application. Figure 3 shows how much more efficient Burst Mode operation is at light loads as compared to pulse skipping mode.

A basic sequencer function can be built for the buck regulator outputs by driving the enable pin on one buck

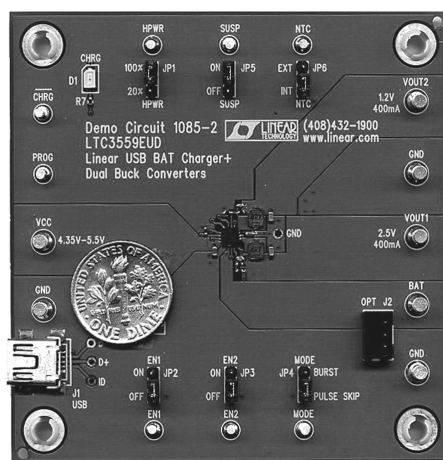



Figure 2. A USB battery charger and two buck regulators small enough to fit in the latest cell phones, PDAs and MP3 players

regulator with the output of the other buck regulator. For proper operation, the BAT and PV_{IN} pins must be tied together. If a buck regulator is enabled while the battery is charging, the net current charging the battery will be lower than the actual programmed value.

Figure 4 helps to explain this scenario. The current being delivered at the BAT pin is 500mA. Both buck regulators are enabled. The sum of the average input currents being drawn by both buck regulators is 200mA. This makes the effective battery charging current only 300mA. If the HPWR pin were tied low, the BAT pin current would be only 100mA. With the buck regulator conditions unchanged, this would cause the battery to discharge at 100mA.

Conclusion

The LTC3559 is ideally suited for space-constrained applications that are powered from a single Li-Ion cell and that need multiple voltage supply rails. The high switching frequency allows the use of small low profile external inductors. The high efficiency buck regulators and Burst Mode operation combine to maximize battery life, extending battery operation time between charge cycles. 

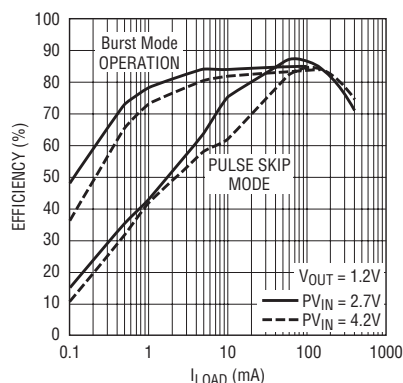


Figure 3. Buck regulator efficiency

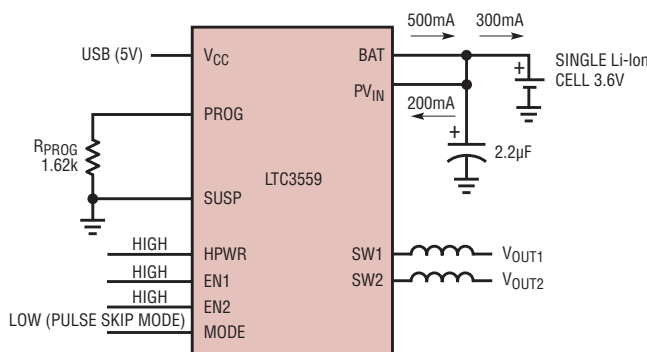


Figure 4. The net current charging the battery depends on the operating mode of the buck regulators.

LT3580, continued from page 10

voltages where these problems might occur.

The shutdown pin comparator has voltage hysteresis with typical thresholds of 1.32V (rising) and 1.29V (falling). Resistor R_{UVLO2} is optional but can be included to reduce overall UVLO voltage variation caused by variations in $\overline{\text{SHDN}}$ pin current. A good choice for R_{UVLO2} is 10k \pm 1%. After choosing a value for R_{UVLO2}, R_{UVLO1} can be determined from either of the following:

$$R_{UVLO1} = \frac{V_{IN+} - 1.32V}{\left(\frac{1.32V}{R_{UVLO2}} \right) + 11.6\mu A}$$

or

$$R_{UVLO1} = \frac{V_{IN-} - 1.29V}{\left(\frac{1.29V}{R_{UVLO2}} \right) + 11.6\mu A}$$

where V_{IN+} and V_{IN-} are the V_{IN} voltages when rising or falling respectively.

Conclusion

The LT3580 is a smart choice for many DC/DC converter applications. It's packed with features without compromising performance or ease of use and is available in tiny 8-lead packages. The accurate and adjustable clock, 2A/42V power switch, wide input voltage range, integrated soft-start and a configurable $\overline{\text{SHDN}}$ pin make the LT3580 an ideal choice for many DC power supply needs. For additional information and a complete data sheet visit www.linear.com. 